

Remarks on cost analysis for patterns of parallelism

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Preliminary notes about Parallel Static Cost Analysis

- ① Static performance analysis: prediction of run time of shapely skeletal parallel programs.
- ② Model: Bounded Synchronous Parallelism (BSP), across various architectures.
- ③ Calculus of skeletons: Bird–Meertens Formalism (BMF).

Notes

	formalism	foundational data structures
•	BSP	arrays
	BMF	lists
•	skeletons \subset parallel patterns	

Research programme

- 1 Develop an equational theory of patterns of parallelism supporting program refinement.
- 2 Develop a cost model that supports comparisons between refinements of a program.
- 3 Develop an algorithm for cost-driven optimal refinement search.

Theory of lists: Bird–Meertens Formalism

Example (Maximum segment sum)

The maximum of the sums of all the segments of a given list:

$$\begin{aligned}
 & mss \\
 = & \quad \{ \text{definition of } mss \} \\
 & \mathit{max}/ \cdot +/* \cdot \mathit{segs} \\
 = & \quad \{ \text{definition of } \mathit{segs} \} \\
 & \mathit{max}/ \cdot +/* \cdot ++/ \cdot \mathit{tails} * \cdot \mathit{inits} \\
 = & \quad \{ \text{distributivity of map and fold} \} \\
 & \mathit{max}/ \cdot (\mathit{max}/ \cdot +/* \cdot \mathit{tails}) * \cdot \mathit{inits} \\
 = & \quad \{ \text{Horner's rule where } x \odot y = \mathit{max} (x + y) 0 \} \\
 & \mathit{max}/ \cdot \odot//_0^L * \cdot \mathit{inits} \\
 = & \quad \{ \text{defining property of left scan} \} \\
 & \mathit{max}/ \cdot \odot//_0^L
 \end{aligned}$$

Where to add cost information to the theory of lists

Problem (Two different intermediate structures of *fold*)

$$/ : (a \rightarrow b \rightarrow a) \rightarrow a \rightarrow [b] \rightarrow a$$

- 1 $(zip +)/ : [a] \rightarrow [[b]] \rightarrow [a]$
pointwise addition of lists; intermediate sums have the same size.
- 2 $++/ : [a] \rightarrow [[b]] \rightarrow [a]$
append of all the given lists; intermediate lists are the same or larger.

Example (Shapes of functions and data)

$$\begin{aligned} \#(zip +) \#x \#y &\stackrel{def}{=} \text{if } \#x = \#y \text{ then } \#x \text{ else error} \\ \#++ \#x \#y &\stackrel{def}{=} ? \end{aligned}$$

Connection with bulk synchronous parallelism

Definition

Shapely program A program is **shapely** if the shape of its result is determined by the shapes of its inputs.

Shape analysis of a non-shapely program F :

- 1 Decompose F into a sequence of subprograms in which all intermediate shapes can be determined but not that of the result.
Try to match subprograms with barrier syncs and data redistributions.
- 2 Determine shapes at the beginning of each subprogram (*reshaping point*).

Shaped language

Type system:

atomic types $\delta ::= int \mid bool \mid float \mid \dots$

array types $\alpha ::= X \mid \delta \mid [\alpha]$

shape types $\sigma ::= \tilde{\delta} \mid \#\alpha$

data types $\tau ::= \alpha \mid \sigma$

phrase types $\theta ::= U \mid \#U \mid exp \tau \mid var \alpha \mid comm \mid \theta \rightarrow \theta$

type schemes $\phi ::= \theta \mid \forall_\alpha X. \phi \mid \forall_\theta U. \phi$

Terms:

$$t ::= x \mid c \mid \lambda x. t \mid t \ t \mid t \text{ where } x = t$$

Examples of shapes

- $m \times n$ matrix of integers:

$$[m, n]^{int} : \#[int]$$

- vector of length m of vectors of length n :

$$[[m, n]^{int}] : \#[[int]]$$

- shape of a map:

$$\#map = \lambda f a. extendShape a (f (entryShape a))$$

where $extendShape a^{\tilde{\delta}} b^{\tilde{\delta}} = [a, b]^{\tilde{\delta}}$.

Parallelisation of a shaped program

- Specify a distribution of data structures across processors, that is, *match the shape of data with the shape of the processor network.*

Definition (Distribution / implicit parallelism)

A (simple) distribution for an array of type $[a]$ is an array of type $[[a]]$ where

- the outer shape: a virtual array of processors;
- the inner shape: the blocks assigned to each of the processors.

Cost analysis

The type $cost = \tilde{float}$ forms a commutative monoid under addition.

Definition (Cost of a superstep in the BSP model)

$$\text{cost of a superstep} = \max W_i + \max h_i * g + L$$

where

- W_i : local processing time on processor i ,
- h_i : number of packets sent or received by the processor i ,
- g : marginal cost of sending a packet (communication throughput / processor throughput),
- L : cost of barrier synchronisation

BSP hardware parameters are (p, L, g) of type $H = size \times cost \times cost$.

The cost of a function $f : [a] \rightarrow [b]$ is given by a function

$$\#f : \#[a] \rightarrow \#[b] \times (H \rightarrow cost)$$

Cost monad

The functor $M\theta = \theta \times (H \rightarrow \text{cost})$ with pointwise operations is a commutative monad.

$$\begin{aligned} M_0(\text{exp } \tau) &= \text{exp } \# \tau \\ M_0(\theta_1 \times \theta_2) &= M_0(\theta_1) \times M_0(\theta_2) \\ M_0(\theta_1 \rightarrow \theta_2) &= M_0(\theta_1) \rightarrow M(M_0(\theta_2)) \end{aligned}$$

The cost of $t : \theta$ is $c(t) : MM_0(\theta)$, for example:

$$\begin{aligned} c(\text{map}) : M(\alpha \rightarrow M\beta) &\rightarrow M([\alpha] \rightarrow [\beta]) \\ c(\text{map}) = \langle \lambda f. \langle \lambda a. \langle \# \text{map } (\text{fst} \cdot f) a, & \\ \lambda h. (\text{snd } (f (\text{entryShape } a)) h) * b(a, h) \rangle, & \\ \lambda h. 0 \rangle \rangle & \end{aligned}$$

where $b(a, h)$ is the size of the blocks of a with respect to the hardware h .

Patterns of data and computations: first approximation

$d ::=$	data structure
\hat{x}	matchable symbol
$d \ t$	compound data
$c ::=$	computation
\hat{x}	matchable symbol
$c \rightarrow t$	compound computation

Pattern calculus

$t ::=$	untyped term
x	variable symbol
\hat{x}	matchable symbol
$t t$	application
$[\theta] t \rightarrow t$	case

matching	$\{u/[\theta] p\}$
match reduction	$([\theta] p \rightarrow s) u \rightsquigarrow \{u/[\theta] p\} s$
Leibniz quality	$\text{eq} = \lambda x. ([\] x \rightarrow \text{true} \mid \lambda y. \text{false})$

Some notation:

$$\lambda x. t \stackrel{\text{def}}{=} [x] \hat{x} \rightarrow t$$

$$\text{car} \stackrel{\text{def}}{=} [x, y] \hat{x} \hat{y} \rightarrow x$$

Divide and conquer in the pattern calculus

Views (additional matching clause):

$$\{u/[\theta] \text{ view } f \ p\} = \{f \ u/[\theta] \ p\}$$

dc *divide combine conquer condition* =

| $[x]$ *condition* $\hat{x} \rightarrow \text{conquer } x$

| $[x, y]$ *view divide* $(\hat{x}, \hat{y}) \rightarrow \text{combine } (f \ x) (f \ y)$

where $f = \text{dc divide combine conquer condition}$

Remarks on implementations-in-progress

- The pattern calculus is capable of complementing BMF in certain important points.
- It is a computationally well-behaved formalism (progress, confluence) and therefore is tangible to interactive theorem proving.
- The approach of the shape calculus where shape types and shape functions form a distinct syntactic category can be taken further and applied to pattern calculus.
- We can use matching and substitution in many novel ways compared to the standard FP, in particular, **instead of dependent types**.

To be continued.